•	Application No.	Applicant(s)
Notice of Allowability	10/785,254	PANG, LINYONG
	Examiner	Art Unit
	Sun J. Lin	2825
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication GHTS. This application is subject t	plication. If not included n will be mailed in due course. THIS
1. This communication is responsive to <u>Amendments filed on 02/02/2006 and 02/13/2006</u> .		
2. X The allowed claim(s) is/are <u>1,3,4,6,7,10-12,14-16 and 18-2</u>	0, renumbered (37 CFR 1.126).	
 Acknowledgment is made of a claim for foreign priority un a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have 		
2. ☐ Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority doc International Bureau (PCT Rule 17.2(a)). * Certified copies not received:	·· —	
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
 A SUBSTITUTE OATH OR DECLARATION must be submi INFORMAL PATENT APPLICATION (PTO-152) which give 		
5. CORRECTED DRAWINGS (as "replacement sheets") mus	t be submitted.	
(a) 🔲 including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment or in the C	Office action of
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the		
 DEPOSIT OF and/or INFORMATION about the depos attached Examiner's comment regarding REQUIREMENT F 	sit of BIOLOGICAL MATERIAL r FOR THE DEPOSIT OF BIOLOGIC	must be submitted. Note the AL MATERIAL.
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5 ☐ Notice of Informal F	Patent Application (PTO-152)
2. ☐ Notice of Neterences Offer (110-032) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary	
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Da	Paper No./Mail Date 7.
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Stateme	ent of Reasons for Allowance
	9. Other	
	'	

Art Unit: 2825

Reasons for Allowance

Claims 1, 3, 4, 6, 7, 10 - 12, 14 - 16 and 18 - 20 are allowed over the prior art of record. An examiner's statement of reasons for allowance is given in the following:

As to Claims 1, 3, 4, 6, 7, 10 - 12, 14 - 16 and 18 - 20, the prior art does not teach or suggest the following subject matters:

- A method of automatically performing a wafer simulation, the method comprising
 obtaining threshold data from a look-up table (LUT), which is generated using a
 model and organized based on feature size, pitch size, and feature/defect
 identification in combination with other limitations as recited in independent
 Claims 1 and 4, respectively;
- A method of determining a wafer contour of a mask feature, the method
 comprising <u>accessing resist information in a look-up table (LUT) to determine a</u>
 <u>threshold associated with the mask feature, the LUT is organized based on</u>
 <u>feature size, pitch size, and feature/defect identification</u> in combination with other
 limitations as recited in independent Claim 7;
- A computer-implemented program for generating a wafer contour, the program
 comprising <u>code for obtaining threshold data from a look-up table (LUT), which is
 generated using a resist model and organized based on feature size, pitch size,
 and feature/defect identification in combination with other limitations as recited in
 independent Claim 12;
 </u>
- A method of creating a look-up table (LUT) for use in a wafer simulation, the
 method including <u>storing thresholds for a plurality of features based on matching</u>
 <u>in the LUT, which is organized based on feature size, pitch size, and</u>
 <u>feature/defect identification</u> in combination with other limitations as recited in
 independent Claim 15.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Application/Control Number: 10/785,254

Art Unit: 2825

Conclusion

Page 3

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sun J. Lin whose telephone number is (571) 272–1899. The examiner can normally be reached on Monday to Friday from 10:00am to 7:00pm.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

James Jun Bir

Sun J. Lin Primary Examiner

Art Unit 2825

March 27, 2006